

Substitute for form 1449A and B/PTO

(use as many sheets as necessary)

Sheet	1	of	2
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Application Number	09/844,675
Filing Date	April 30, 2001
First Named Inventor	ROBERTS, Gordon W.
Art Unit	2858
Examiner Name	Albert Decady
Attorney Docket Number	1770-254US JEL/cp

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Phup Chup

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¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A and B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known			
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
me	1.	A. DANCY and A. CHANDRAKASAN, "A Reconfigurable Dual Output Low Power Digital PWM Power Converter", <i>Proc. IEEE International Symposium Low Power Electronics and Design</i> , pp. 191-196, August, 1998	
me	2.	B. DUFORT and G. W. ROBERTS, "Signal Generation Using Periodic Single and Multi-Bit Sigma-Delta Modulated Streams", <i>Proc. IEEE International Test Conference</i> , pp. 396-405, Washington, D.C., November, 1997	
me	3.	S. D. GRIGORESCU, C. ILIESCU and B. PANTELIMON, "Sigma-Delta Modulation and PWM Performs High-Resolution DAC for DC Applications", <i>Conference on Precision Electromagnetic Measurements</i> , pp. 45-46, 1996	
me	4.	A. PIERAZZI, A. BONI and C. MORANDI, "Band-Gap References for Near 1-V Operation in Standard CMOS Technology", <i>IEEE 2001 Custom Integrated Circuits Conference</i> , pp. 463-466, San Diego, CA, May, 2001	
me	5.	G. A. RINCON-MORA and P. E. ALLAN, "A 1.1-V Current Mode and Piecewise-Linear Curvature-Corrected Bandgap Reference", <i>IEEE Journal of Solid-State Circuits</i> , Vol. 33, #10, pp. 1551-1554, Oct. 1998	
me	6.	M. M. HAFED, S. LABERGE and G. W. ROBERTS, "A Robust Deep Submicron Programmable IDC Voltage Generator", <i>IEEE International Symposium on Circuits and Systems</i> , pp. IV-5-IV-8, May 28-31, 2000 Geneva Switzerland	

Examiner Signature	<i>Phung Chung</i>	Date Considered	2/5/04
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